

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Borchert, Bernd

PATENT

Serial No: 09/944823

COPY OF PAPERS
ORIGINALLY FILED

Art Group Unit:

Filing Date: August 31, 2001

Examiner:

Title: Halbleiterlaserchip und Verfahren
zum Hartselle eines
Halbleiterlaserchips

FILING OF PRIORITY
DOCUMENT AND
TRANSLATION

Honorable Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

Enclosed herein please find the following documents relating to the referenced pending U.S. Patent Application:

1. Priority Document No. 100 42 904.1.
2. An accurate translation of the application in English. A check in the amount of \$130.00 pursuant to 37 C.F.R. §1.17(i) is enclosed.
3. Please charge any further amounts required by this filing to Deposit Account No. 02-3732 or credit any overpayments thereto.

EXTENSION OF TIME

Applicant believes that no time extension is necessary in order to make this response timely. Should Applicant be in error, Applicant respectfully requests such time extension as necessary to make this response timely and hereby authorizes the U.S. Patent and Trademark Office to charge Deposit Account 02-3732 for any necessary fees in respect to said time extension.

Respectfully submitted,
by their attorney,

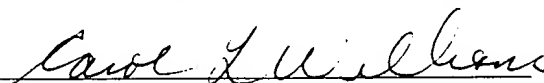
Date: 11/5/2001

By: 

Craig Gregersen
Briggs and Morgan
2200 1st National Bank Building
332 Minnesota Street
St. Paul, MN 55101

CERTIFICATE OF MAILING UNDER 37 CFR 1.8: I hereby certify that this paper and any papers referred to herein are being deposited with the U.S. Postal Service, as first class mail, postage prepaid, addressed to the Commissioner of Patents and Trademarks, c/o Assistant Commissioner of Patents, Washington, D.C. 20231 on

Nov. 5, 2001


Signature